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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Zhaoyun Xing and Russell Kao

Title: SYSTEMS AND METHODS FOR LINEAR MINIMUM CONVOLUTION

Application No.: 09/998,405

Filed: November 30, 2001

Examiner: (not yet assigned)

Group Art Unit: 2121

Atty. Docket No.: 004-5621

May 31, 2002

COMMISSIONER FOR PATENTS  
Washington, DC 20231

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- ☒ Form(s) PTO-1449  
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Michael P. Noonan

May 31, 2002  
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Respectfully submitted,



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U.S. Department of Commerce, Patent and Trademark Office		Attorney Docket No.: 004-5621
		Application No.: 09/998,405
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use several sheets if necessary)		Applicant(s): Zhaoyun Xing et al.
		Filing Date: Nov. 30, 2001
		Group Art Unit: 3629
		Date Submitted: May 31, 2002
<b>NON PATENT LITERATURE DOCUMENTS</b>		
*Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
	AA	Arnold, M. H. and Scott, W. S., "An Interactive Maze Router with Hints," 25th ACM/IEEE Design Automation Conference, 1988, Paper 41.4, pp. 672-676.
	AB	Tsai, Chia-Chun et al., "An H-V Alternating Router," IEEE Transactions on Computer-Aided Design, Vol. 11, No. 8, Aug. 1992, pp. 976-991.
	AC	Margarino, A. et al., "A Tile-Expansion Router," IEEE Transactions on Computer-Aided Design, Vol. CAD-6, No. 4, Jul. 1987, pp. 507-517.
	AD	Liu, Le-Chin E. et al., "Chip-Level Area Routing," International Symposium on Physical Design, Proceedings of the 1998 International Symposium on Physical, Monterey, CA, 1998, ACM Press, NY, NY pp. 197-204.
	AE	Cong, Jason et al., "An Implicit Connection Graph Maze Routing Algorithm for ECO Routing," ACM SIGDA proceedings 1999, Session 3B: Routing, 5 pp.; [online] [Downloaded from the Internet May 14, 2002, URL: < <a href="http://www.sigda.org/Archives/ProceedingArchives/lccad/lccad99/papers/1999/lccad99/pdffiles/03b_2.pdf">http://www.sigda.org/Archives/ProceedingArchives/lccad/lccad99/papers/1999/lccad99/pdffiles/03b_2.pdf</a> >.
	AF	Dion, J. and Monier, L. M., "Contour: A Tile-based Gridless Router," WRL Research Report 95/3, Digital Western Research Laboratory, Palo Alto, CA, March 1995, 30 pp. [online] [Downloaded from the Internet May 14, 2002, URL: < <a href="ftp://gatekeeper.research.compaq.com/pub/DEC/WRL/research-reports/WRL-TR-95.3.pdf">ftp://gatekeeper.research.compaq.com/pub/DEC/WRL/research-reports/WRL-TR-95.3.pdf</a> >.
	AG	Wu, Ying-Fung et al., "Rectilinear Shortest Paths and Minimum Spanning Trees in the Presence of Rectilinear Obstacles," IEEE Transactions on computers, Vol. C-36, No. 3, Mar. 1987, pp. 321-331.
	AH	Zheng, S. Q. et al., "Finding Obstacle-Avoiding Shortest Paths Using Implicit Connection Graphs," IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 15, No. 1, Jan. 1996, pp. 103-110.
	AI	
	AJ	
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